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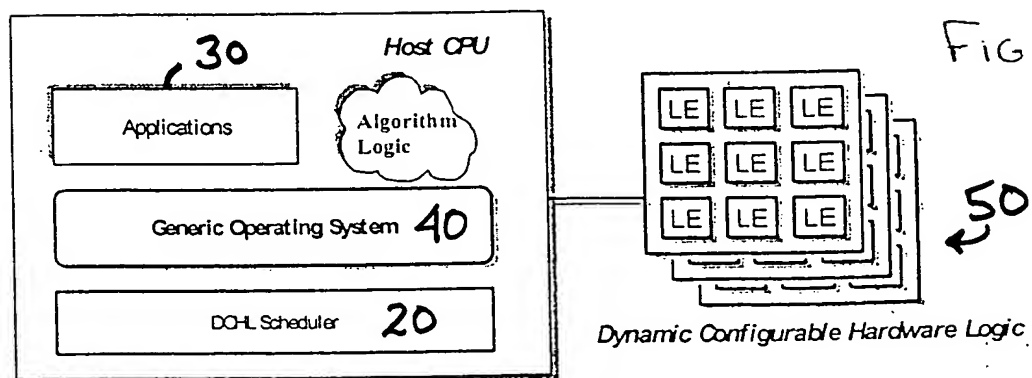
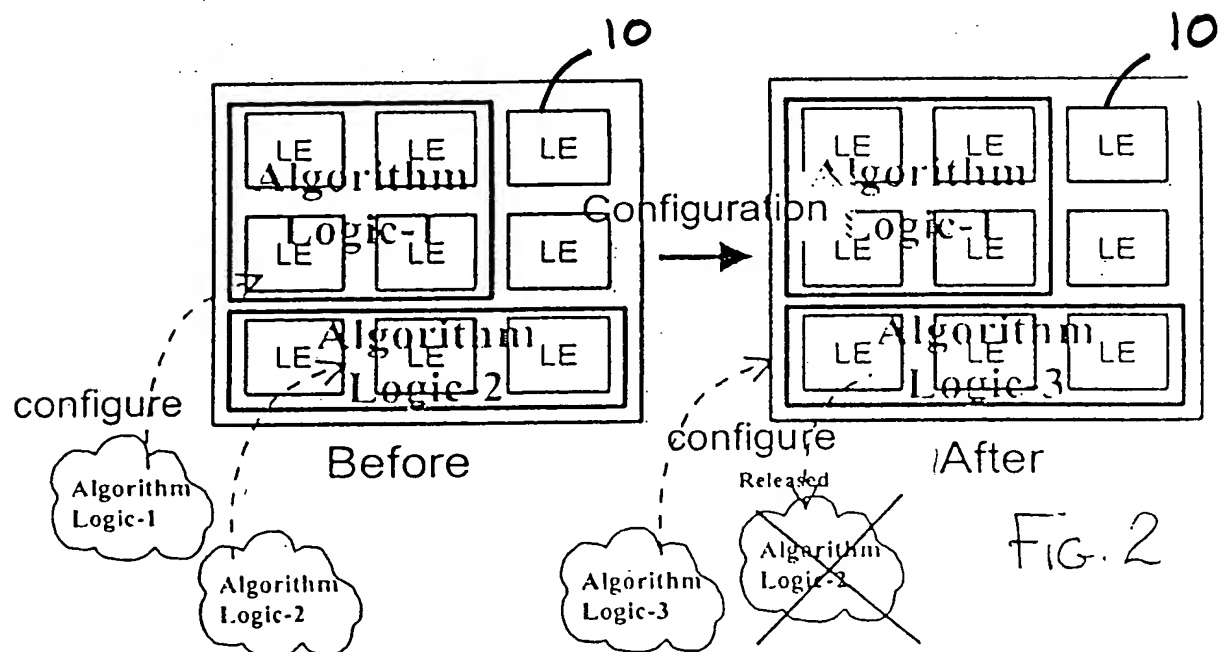
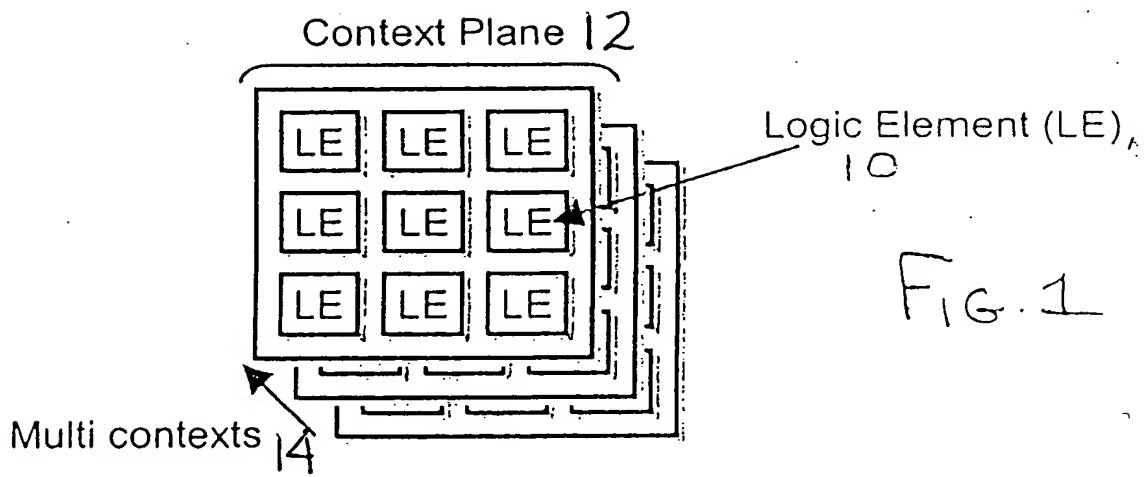


FIG. 4A

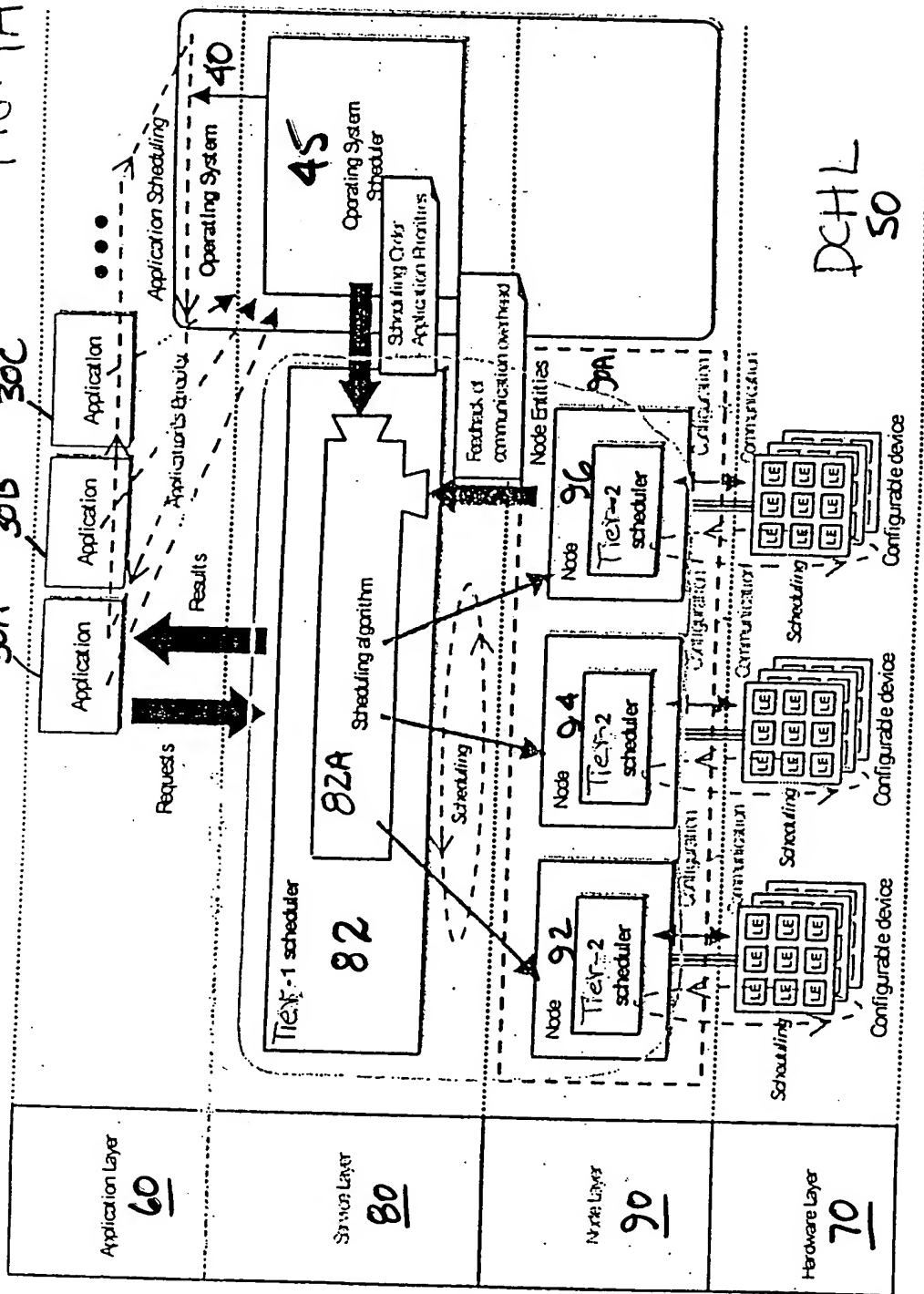


FIG. 4B

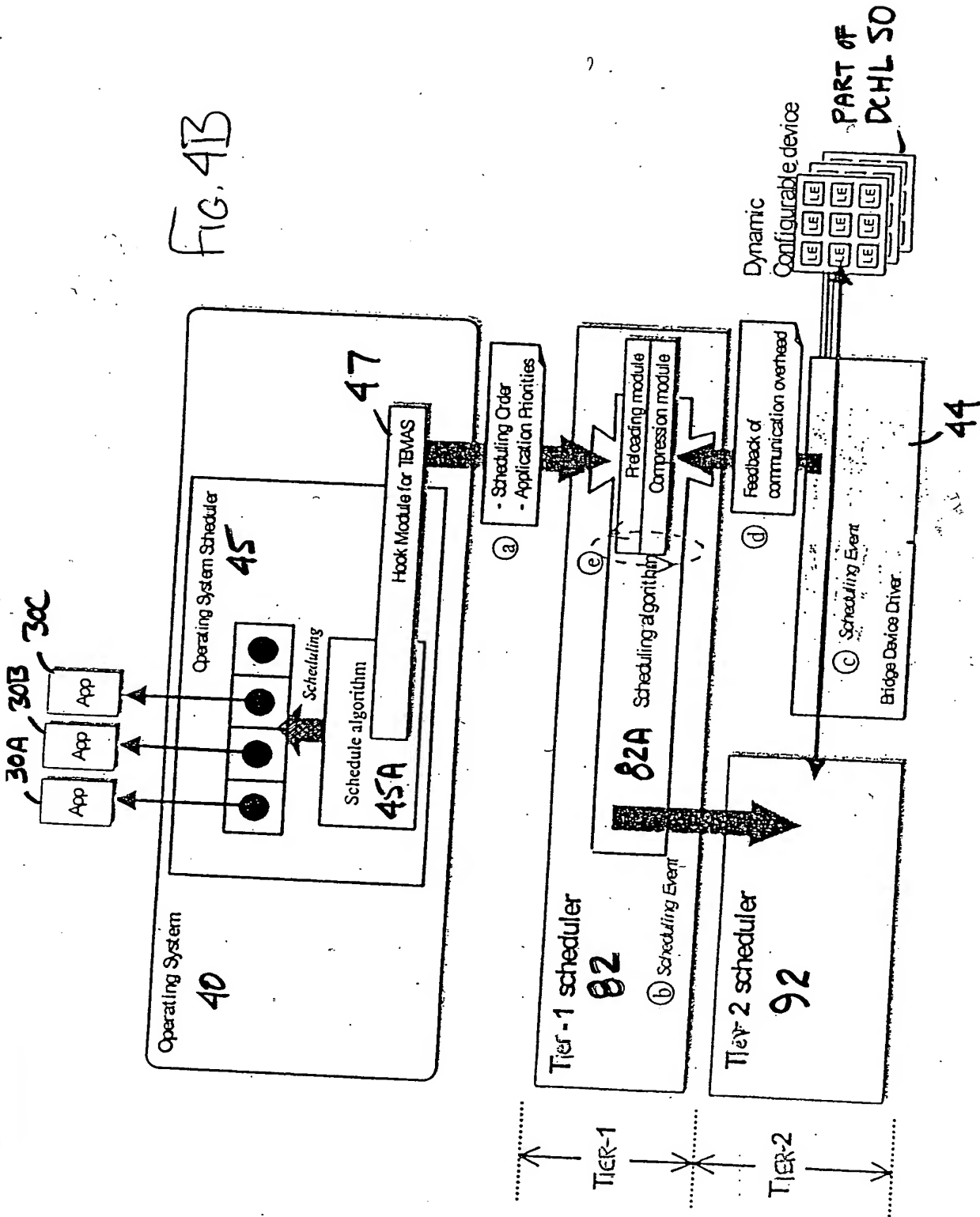
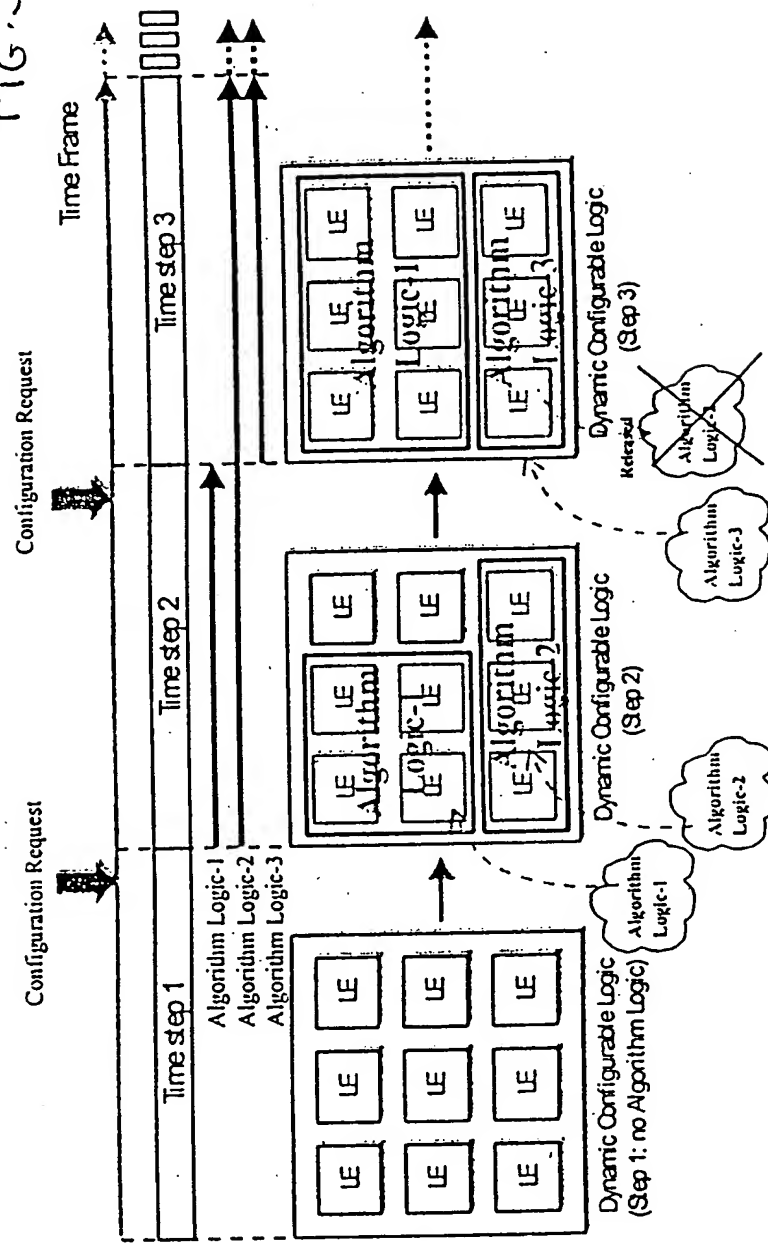


FIG. 5



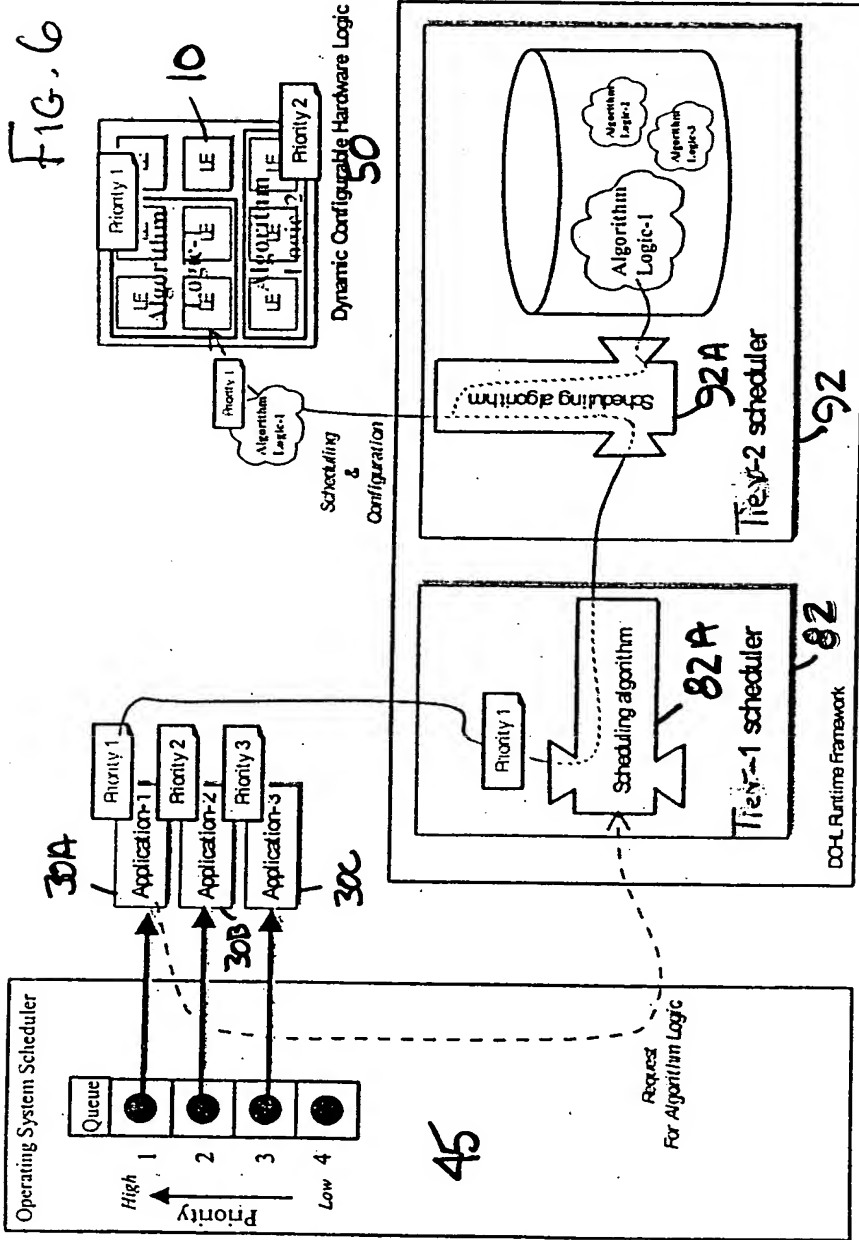
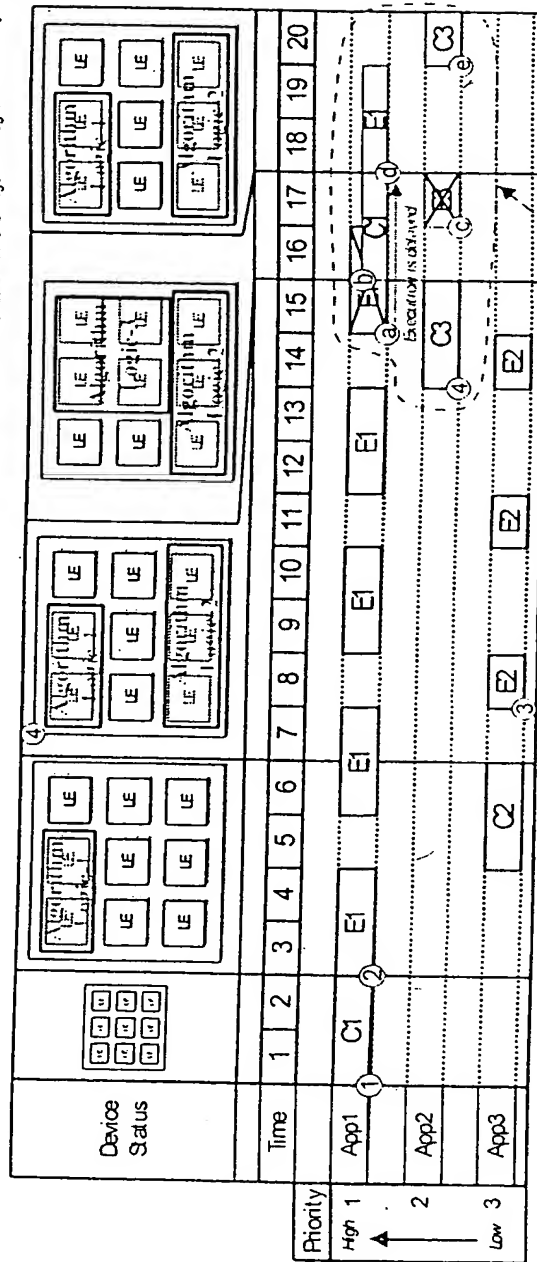


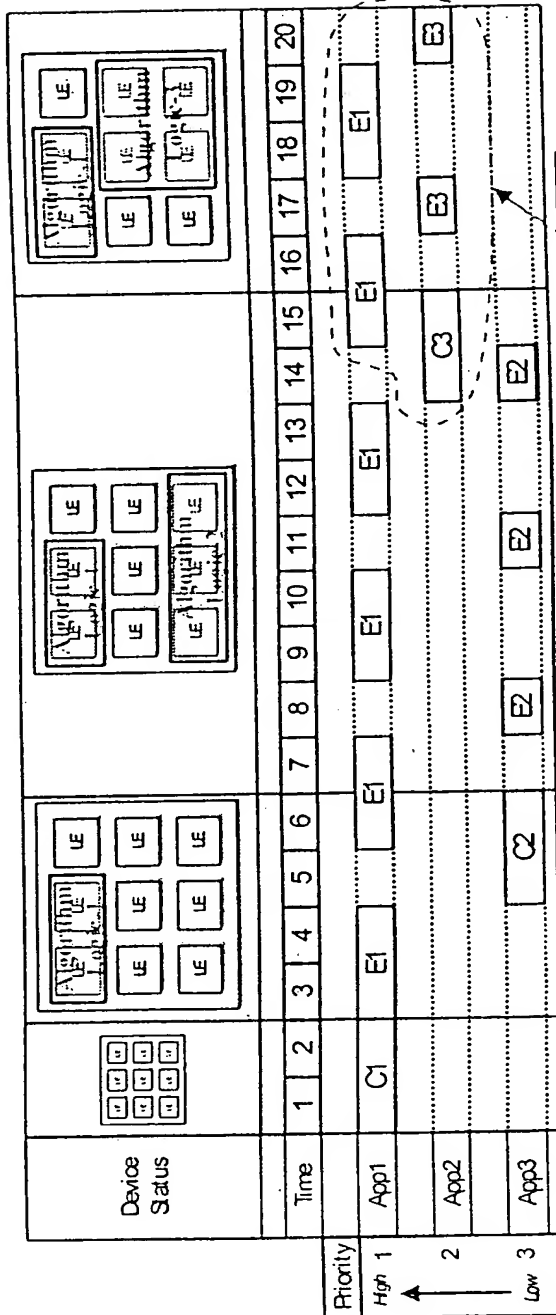
FIG. 7

[Conventions]  
 App: Application working on Host CPU  
 Cx: Configuration line for AlgorithmLogic x  
 Ex: Execution line for AlgorithmLogic x



The Algorithm logic attached to application having the highest priority is delayed to work for from (a) to (d). Additionally, Algorithm-3 misses its execution at (c). Extra inefficient configuration both (b) and (e) are performed.

[Overlapped]  
 App: Application working on I/O  
 Cx: Configuration time for Algorithm Logic x  
 E: Execution time for Algorithm Logic x



Algorithm logics attached to applications having higher priority are working fine, which means that execution delay and extra configuration doesn't occur. The PI ensures the best utilization.

FIG. 8



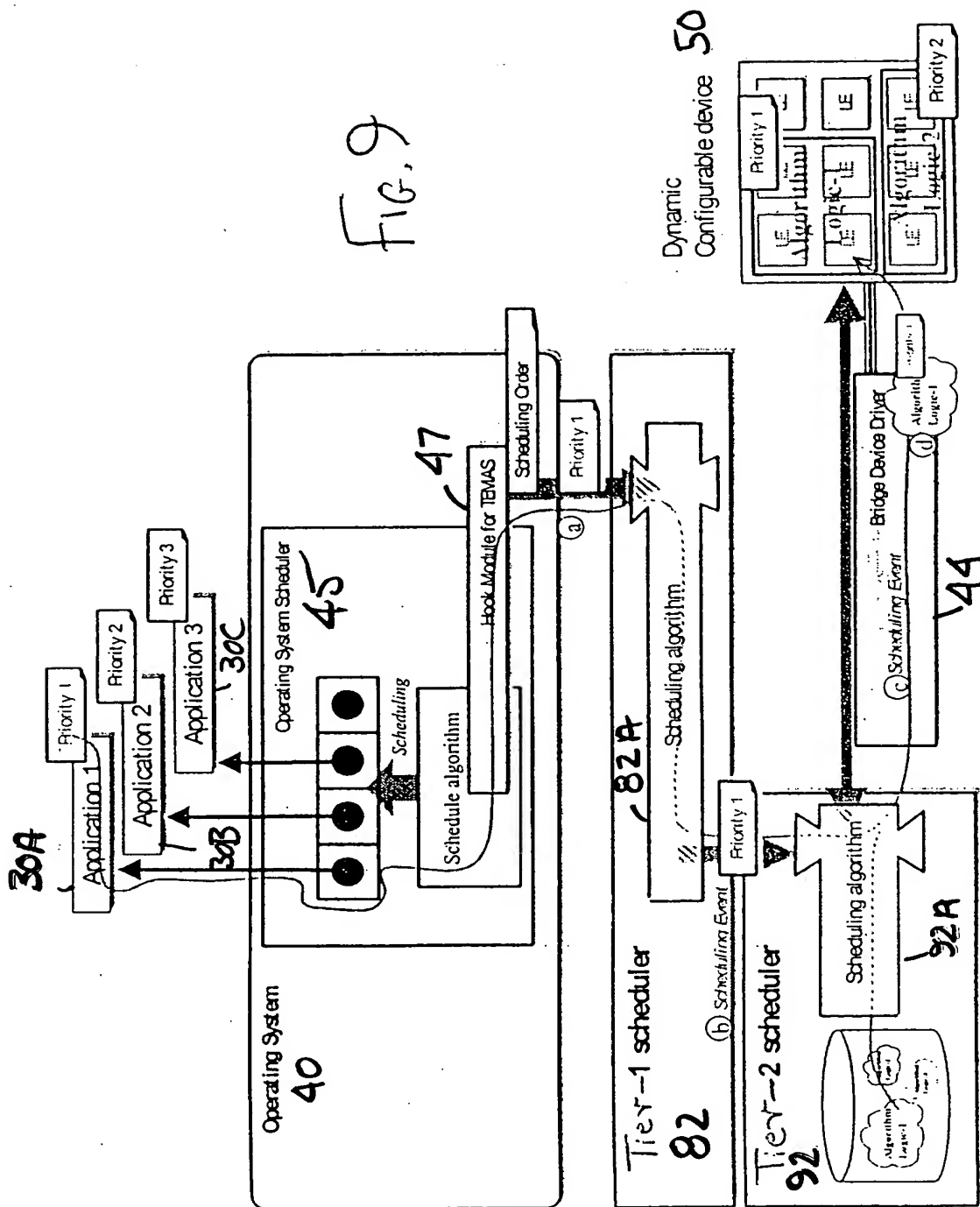


FIG. 9

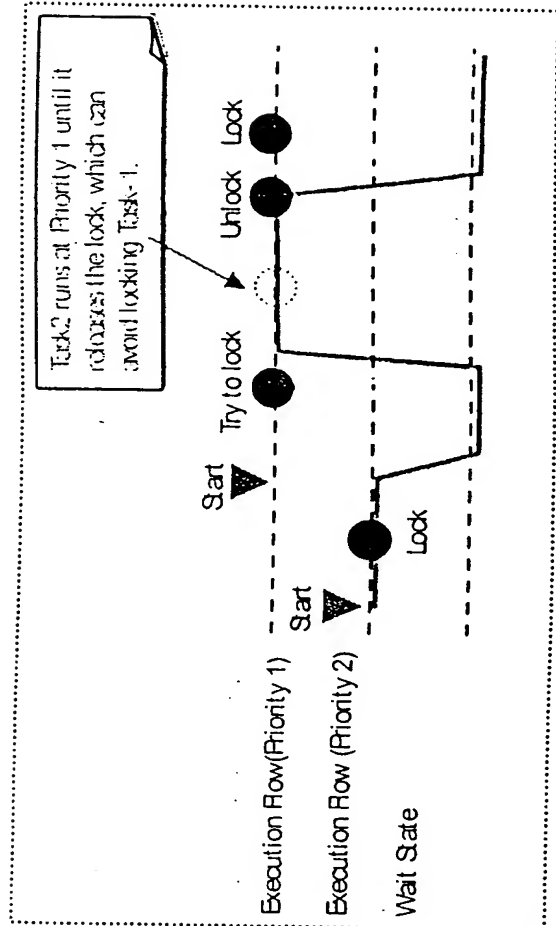
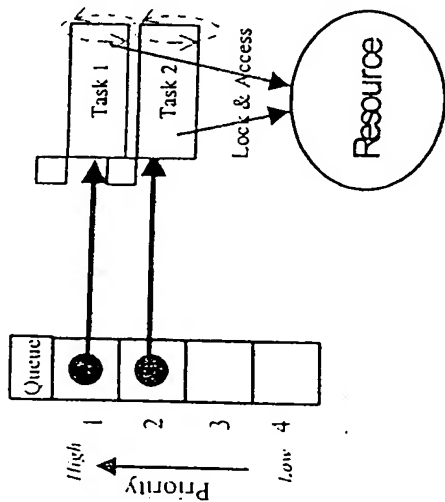


FIG. 1D  
PRIOR ART

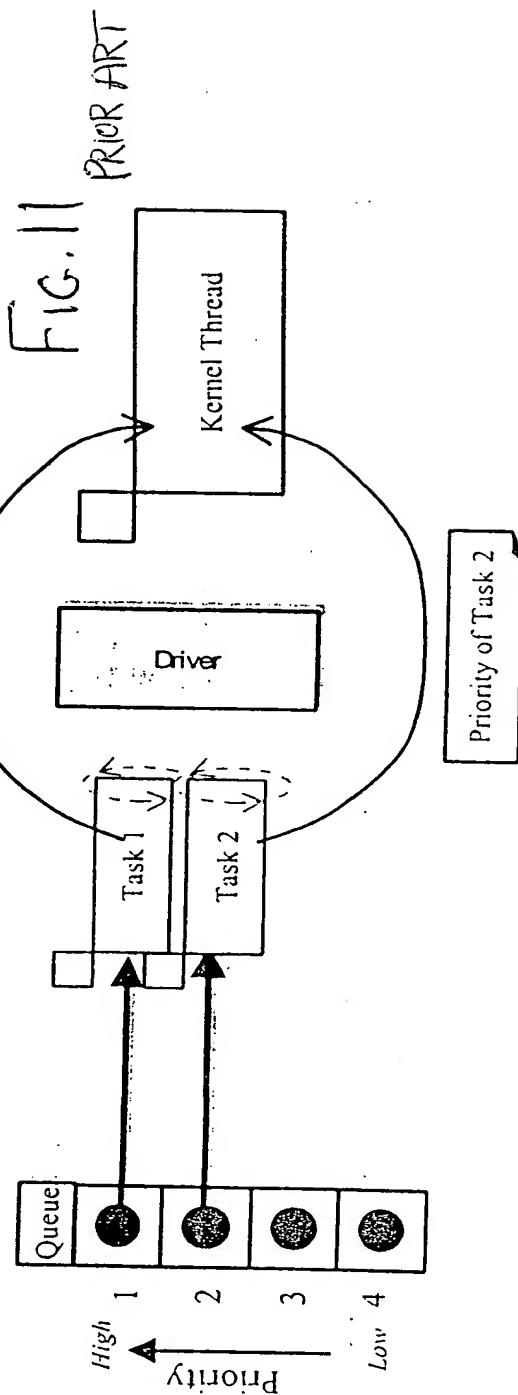


FIG. 1I  
PRIOR ART

FIG. 12  
PRIOR ART

